

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chen et al.

Title: Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memories

Application No.: 10/552,948 Filing Date: September 11, 2006

Examiner: Amir Zarabian Group Art Unit: 2827

Docket No.: SNDK.284US1 Conf. No.: 6753

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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE TO OF NON-COMPLIANT AMENDMENT (37 CFR 1.121)**

Dear Sir:

In response to the "Notice of Non-Compliant Amendment" mailed by the United States Patent and Trademark Office on January 23, 2007, the new paragraph added in the Preliminary Amendment filed on September 11, 2006 is hereby submitted without underlining as follows.

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks** begin on page 3.

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FILED VIA EFS

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